

 **B-TECH VLSI PROJECTS LIST (2019-20)**

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| **S.NO** | **PROJECT TITLE** | **DESIGN** |
|  | A Low-Power Parallel Architecture forLinear Feedback Shift Registers | Front End |
|  | FSM based High Speed VLSI Architecture for DBUTVF Algorithm | Front End |
|  | Machine Learning based Power Efficient Approximate 4:2 Compressors for Imprecise Multipliers | Front End |
|  | A Double Error Correction Code for 32-bit Data Words with Efficient Decoding | Front End |
|  | A Low Power Binary Square Rooter using Reversible Logic | Front End |
|  | A New Logic for Implementation of Digital Error Correction Block | Front End |
|  | Fast & Energy Efficient Binary to BCD Converter with Complement Based Logic Design (CBLD) for BCD Multipliers | Front End |
|  | Design of 32-bit MIPS ALU by Efficient Adders | Front End |
|  | FPGA Based 64-Bit Low Power RISCProcessor Using Verilog HDL | Front End |
|  | Area and Time Efficient Square Architecture | Front End |
|  | Modified Binary Multiplication Circuit Based On Vedic Mathematics | Front end |
|  | An Efficient Design Of 16 Bit MAC Unit Using Vedic Mathematics  | Front end |
|  | 16 bit power efficient carry select adder | Front end |
|  | Dual-quality 4:2 Compressors For Utilizing In Dynamic Accuracy Configurable Multipliers | Front End |
|  | Pre-Encoded Multipliers Based on Non-Redundant Radix-4 Signed-Digit Encoding | Front end |
|  | Design of Multiplier less Multiple Constant Multiplication for Convolution Circuits | Front end |
|  | A High-Performance FIR Filter Architecture forFixed and Reconfigurable Applications | Front end |
|  | Reliable Low-Power Multiplier Design UsingFixed-Width Replica Redundancy Block | Front end |
|  | A Method to Design Single Error Correction CodesWith Fast Decoding for a Subset of Critical Bits | Front end |
|  | Analysis of Adiabatic flip-flops for Ultra LowPower Applications. | Back end |
|  | Design of Low-Power High-Performance 2–4 and 4–16 Mixed-Logic Line Decoders | Back end |
|  | Low power area efficient ALU with low power full adder | Back end |

**Head Office**: #202 2nd Floor, Pancom Business Center Opp, Chennai Shopping Mall, Ameerpet, Hyderabad 040-44433434, Mail-id: ramu.krest@gmail.com,www.kresttechnology.com

B**ranches:** Warangal (9000739460), Vijayawada (9000404182)